



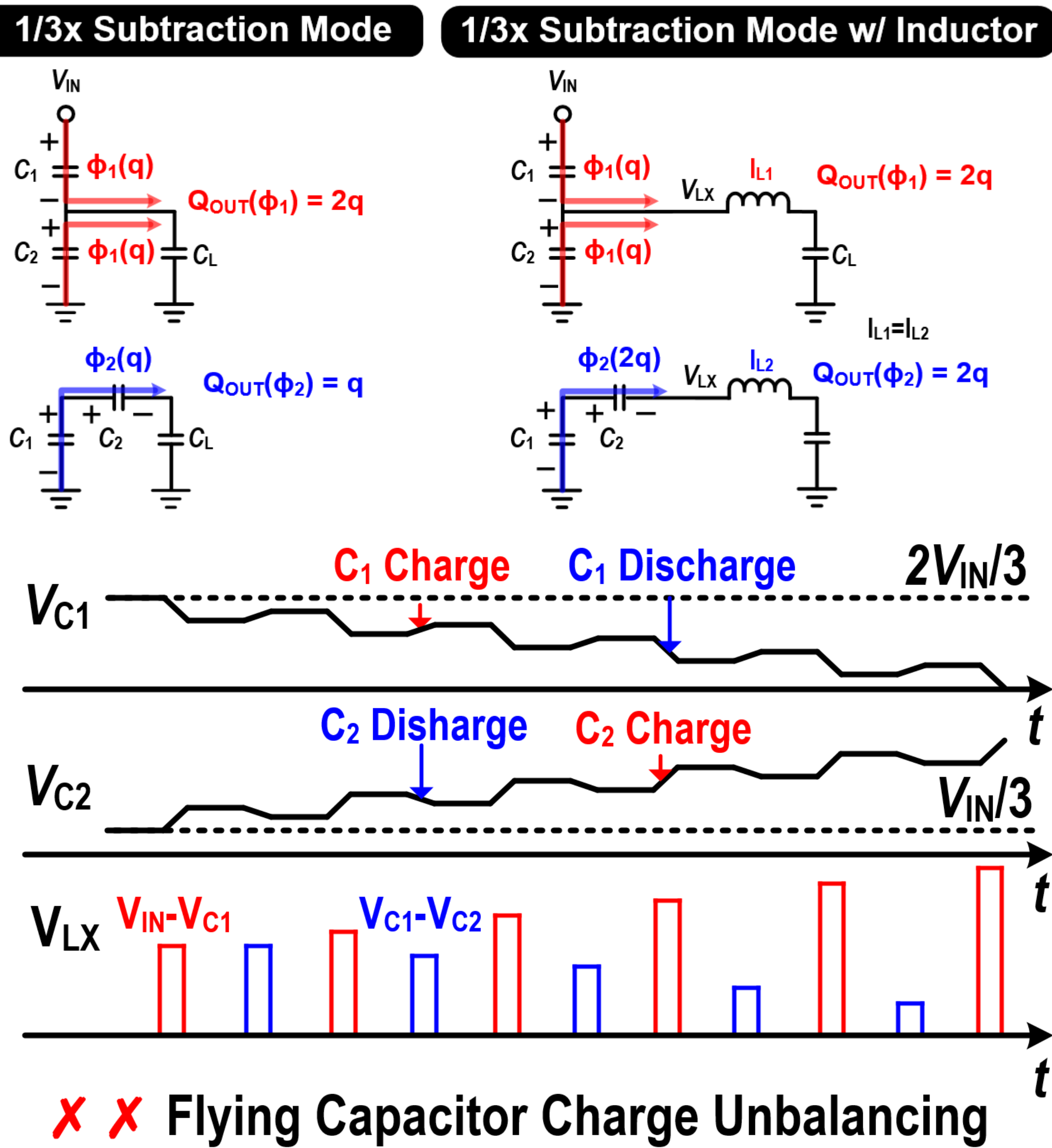
2024 IDEC CDC – TSMC180 – 2302

A 90.5% Peak Efficiency 12V-to-1V Fully Soft-Charging Hybrid Subtraction Mode Converter with Active Voltage Balancing

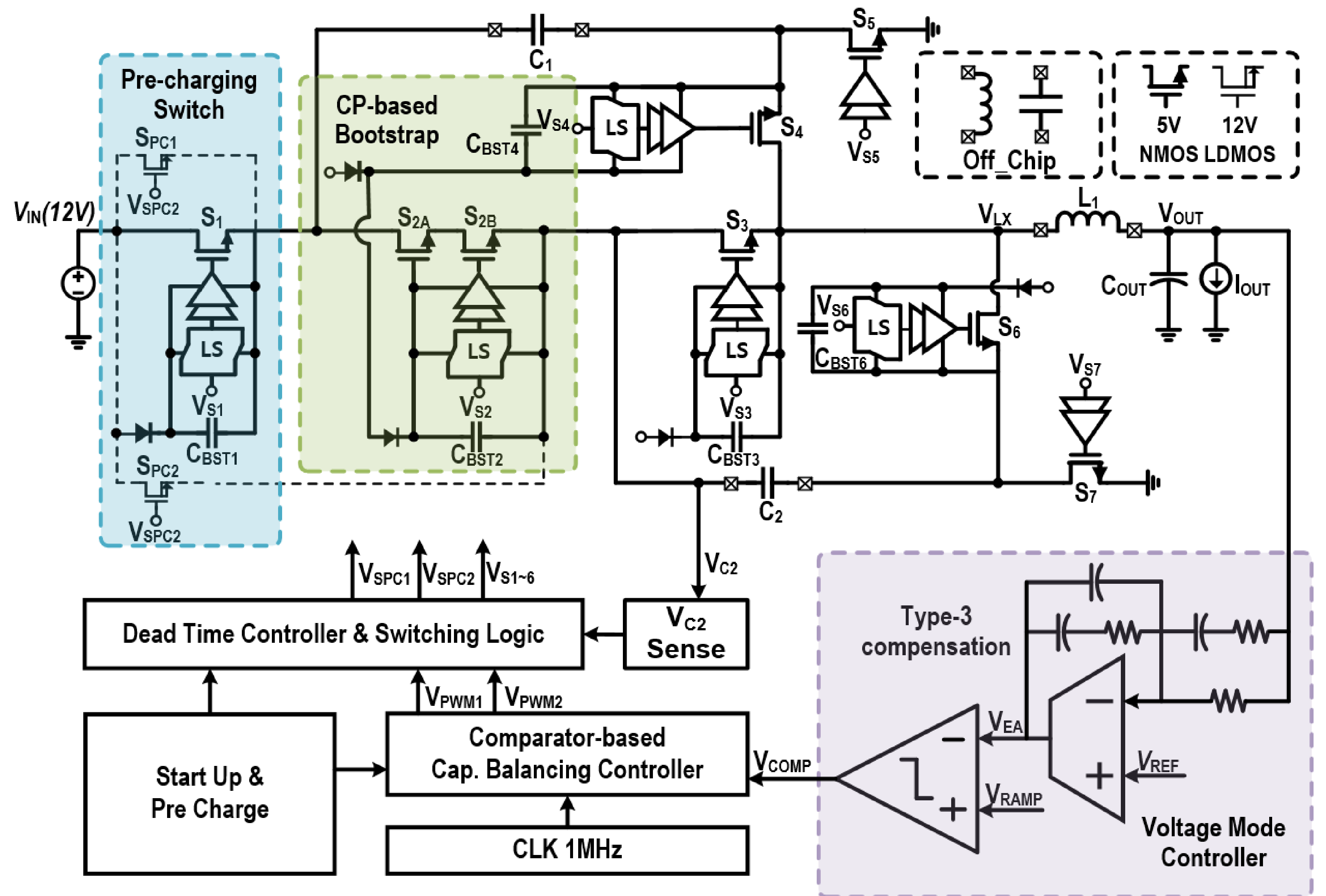
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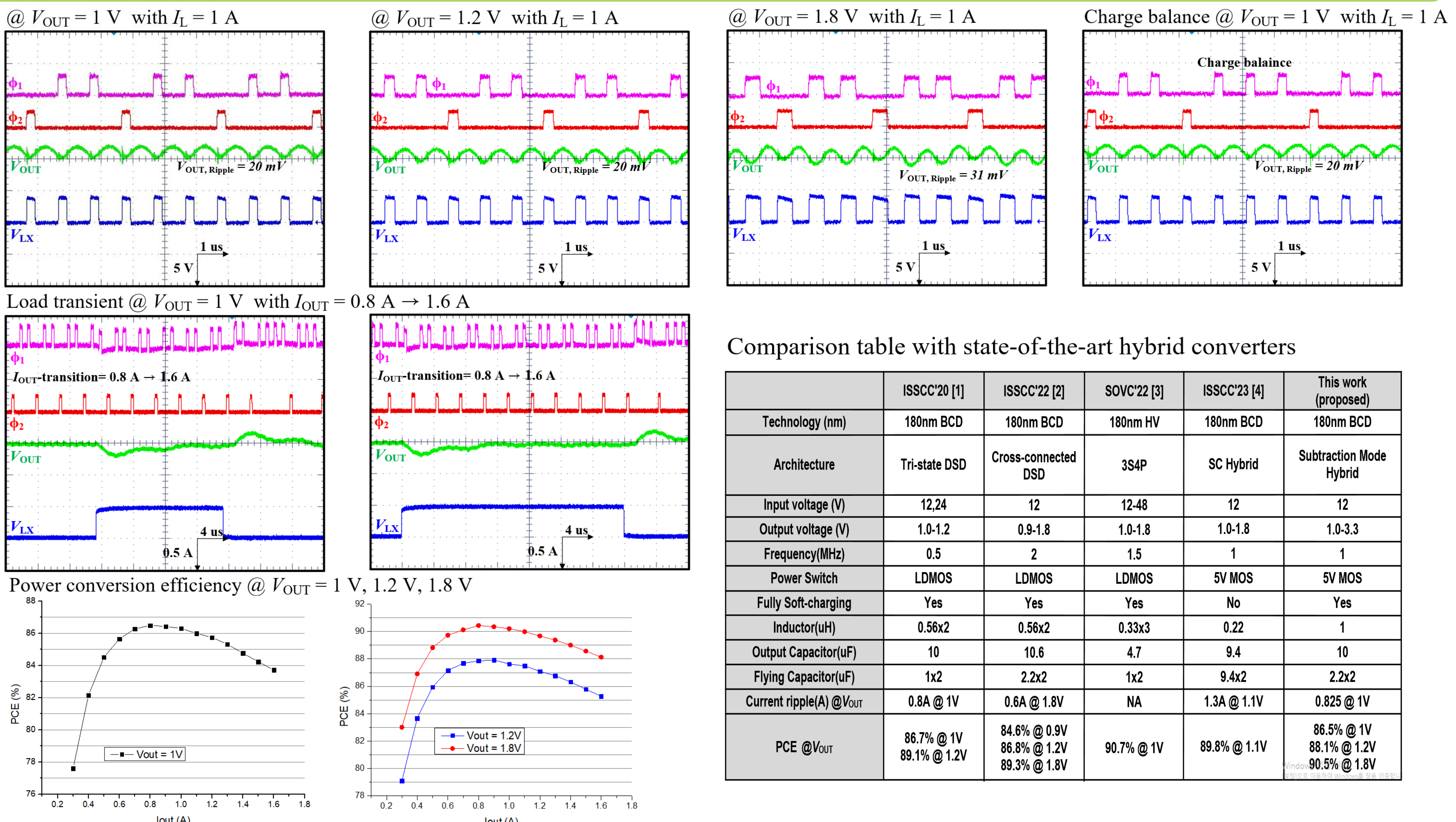
Conventional SC stage



Proposed HSM Converter



Measurement Results



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